

IN THE CLAIMS

The current form of the claims is as follows:

1. (Original) A method for detecting corruption associated with a stack in a storage device, the method comprising the steps of:

inserting a quantity of information adjacent to the stack in the storage device, the quantity of information having an initial state; and

inspecting the quantity of information so as to identify any deviation from the initial state and thereby detect corruption associated with the stack in the storage device.

2. (Original) The method as defined in claim 1, wherein the initial state of the quantity of information represents a bit pattern.

3. (Original) The method as defined in claim 1, wherein the initial state of the quantity of information represents a processor readable address.

4. (Original) The method as defined in claim 1, wherein the initial state of the quantity of information represents a processor readable instruction.

5. (Original) The method as defined in claim 1, further comprising the step of:
adding data to the stack after inserting the quantity of information adjacent to the stack in the
storage device.

6. (Original) The method as defined in claim 5, wherein the data is added during a push
operation, further comprising the step of:
recording the push operation after identifying any deviation from the initial state.

7. (Original) The method as defined in claim 1, further comprising the step of:
removing data from the stack after inserting the quantity of information adjacent to the stack
in the storage device.

8. (Original) The method as defined in claim 7, wherein the data is removed during a
pop operation, further comprising the step of:
recording the pop operation after identifying any deviation from the initial state.

9. (Original) The method as defined in claim 1, further comprising the step of:
restoring the quantity of information to the initial state after identifying any deviation from
the initial state.

10. (Original) The method as defined in claim 1, wherein the step of inserting the quantity of information adjacent to the stack in the storage device includes:

inserting a first quantity of information adjacent to a top of the stack in the storage device;

and

inserting a second quantity of information adjacent to a bottom of the stack in the storage device.

11. (Original) A system for detecting corruption associated with a stack in a storage device, the system comprising:

a processor; and

a storage medium for storing instructions that are readable by the processor and thereby cause the processor to operate so as to:

insert a quantity of information adjacent to the stack in the storage device, the quantity of information having an initial state; and

inspect the quantity of information so as to identify any deviation from the initial state and thereby detect corruption associated with the stack in the storage device.

12. (Original) The system as defined in claim 11, wherein the initial state of the quantity of information represents a bit pattern.

13. (Original) The system as defined in claim 11, wherein the initial state of the quantity of information represents a processor readable address.

14. (Original) The system as defined in claim 11, wherein the initial state of the quantity of information represents a processor readable instruction.

15. (Previously Presented) The system as defined in claim 11, wherein the instructions further cause the processor to operate so as to:

add data to the stack after inserting the quantity of information adjacent to the stack in the storage device.

16. (Previously Presented) The system as defined in claim 15, wherein the data is added during a push operation, the instructions further causing the processor to operate so as to:

record the push operation after identifying any deviation from the initial state.

17. (Previously Presented) The system as defined in claim 11, wherein the instructions further cause the processor to operate so as to:

remove data from the stack after inserting the quantity of information adjacent to the stack in the storage device.

18. (Previously Presented) The system as defined in claim 17, wherein the data is removed during a pop operation, the instructions further causing the processor to operate so as to:

record the pop operation after identifying any deviation from the initial state.

19. (Previously Presented) The system as defined in claim 11, wherein the instructions further cause the processor to operate so as to:

restore the quantity of information to the initial state after identifying any deviation from the initial state.

20. (Previously Presented) The system as defined in claim 11, wherein the instructions further cause the processor to operate so as to:

insert a first quantity of information adjacent to a top of the stack in the storage device; and

insert a second quantity of information adjacent to a bottom of the stack in the storage device.

21. (Original) A computer system including a mechanism for detecting corruption associated with a stack in a storage device, the computer system comprising:

a computer readable storage medium; and

computer programming stored on a storage medium;

wherein the stored computer programming is configured to be readable from the computer readable storage medium by one or more computers and thereby cause the one or more computers to

operate so as to:

insert a quantity of information adjacent to the stack in the storage device, the quantity of information having an initial state; and

inspect the quantity of information so as to identify any deviation from the initial state and thereby detect corruption associated with the stack in the storage device.

22. (Original) The computer system as defined in claim 21, wherein the initial state of the quantity of information represents a bit pattern.

23. (Original) The computer system as defined in claim 21, wherein the initial state of the quantity of information represents a processor readable address.

24. (Original) The computer system as defined in claim 21, wherein the initial state of the quantity of information represents a processor readable instruction.

25. (Previously Presented) The computer system as defined in claim 21, wherein the stored computer programming further causes the one or more computers to operate so as to:

add data to the stack after inserting the quantity of information adjacent to the stack in the storage device.